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Roll No.

CS-601

B. E. (Sixth Semester) EXAMINATION, June, 2012

(Computer Science & Engg. Branch)

MICROPROCESSOR AND INTERFACING

(CS-601)

Time : Three Hours

Maximum Marks : 100

Minimum Pass Marks : 35

Note : Attempt all questions. All questions carry equal marks.

1. (a) What do you mean by pipelined architecture ? How is it implemented in 8086 ? 10
(b) Draw and discuss the read and write cycle timing diagram of 8086 in maximum mode. 10

Or

2. (a) Give the *four* operations commonly performed by the MPU. 10
(b) What are the interrupts available in 8086 ? Give the interrupt vector table of 8086 microprocessor. 10
3. (a) Explain the stack structure of 8086 in detail. 10
(b) Write a program in assembly language for 8086 to insert a delay of 20 msec. Assume the operating frequency 8086 is 8 MHz. 10

P. T. O.

9. (a) What are the types of shift registers ? Explain the function of each type. 10
(b) Differentiate between EPROM and EEPROM. 10

Or

10. Write short notes on the following : 20
(i) Magnetic memory
(ii) Cache memory
(iii) Real and Virtual Memory Buses
(iv) Memory Addressing Capacity of CPU